# ZETTLER DISPLAYS

# SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

	CUSTOMER APPROV	VAL	
※ PART	.: <u>ATM0177B5 (ZETTL</u>	LER DISPLA	YS) VER3.2
APPROVAL	С	COMPANY	,
		СНОР	
CUSTOMER COMMENTS			

ZETTLER DIS	ZETTLER DISPLAYS ENGINEERING APPROVAL							
DESIGNED BY CHECKED BY APPROVED BY								

# ATM0177B5(ZETTLER DISPLAYS)TFT MODULE VER3.2

REVISION	REVISION DATE	PAGE	CONTENTS
VER1.0	2014-08-06		FIRST ISSUED
VER2.0	2015-01-08		REVISE FPC DESIGN
			CORRENT BRIGHTNESS
VER3.0	2015-08-05		CHANGE FPC DIMENSION, CHANGE
			DOUBLE SIDE ADHESIVE
VER3.1	2015-11-5		CHANGE MODULE FPC BEND LENGTH AN
			PAD
			CHANGE BL FPC LENGTH AND PAD
VER3.2	2015-12-14	14	INCREASE THE VIA INNER DIAMETER OF
			PAD FROM 0.25MM TO 0.3MM
			INCREASE THE VIA OUTSIDE DIAMETER OF PAD FROM 0.5MM TO 0.55MM

#### **※ CONTENTS**

- 1. GENERAL SPECIFICATIONS
- 2. PIN ASSIGNMENT
- 3. OPERATING SPECIFICATIONS
- 4. OPTICAL SPECIFICATIONS
- 5. RELIABILITY TEST
- 6. PRECAUTION FOR USING LCM
- 7. MECHANICAL DRAWING

# **1. GENERAL SPECIFICATIONS**

ltem	Specification	Remark
1. LCD size	1.77 inch(Diagonal)	
2. Driver element	a-Si TFT active matrix	
3. Resolution	128x(RGB)x160	
4. View Direction	12 O'clock	Best image
5. Display mode	Normally white , Transmissive	
6. Dot Pitch (W*H)	0.073mm(W) x 0.219mm(H)	
6. Pixel pitch(W*H)	0.219mm(W) x 0.219mm(H)	
7. Active Area(W*H)	28.032mm(W) x 35.04mm(H)	
8. Module size (W*H)	34.70mm(W) x 46.7mm(H) x 2.5mm Max(D)	Note 1
9. Surface treatment	Anti-glare	
10. Color arrangement	RGB-stripe	
11. Color	65K	
12. Controller	ST7735S	
13. Interface	MCU interface	
14. Weight	T.B.D.	
15. RoHS	RoHS compliant	

Note 1: Please refer to mechanical drawing.

# 2. PIN ASSIGNMENT

Pin No.	Symbol	Function	Level	Note
1	GND	Power ground	Р	
2	LEDK1	Back light cathode	Р	
3	LEDA	Back light anode	Р	
4	LEDK2	Back light cathode	Р	
5	VCC	Power supply	Р	
6	ID	No connection		
7	IMO	System interface select. Low: 8-bit bus interface, High: 16-bit bus interface	I	
8	CS	Chip selection	I	
9-24	DB15-DB0	Data bus	I	
25	RD	Read signal	I	
26	WR	Write signal	I	
27	RS	Command/Data select signal	I	
28	REST	Reset	I	
29	IOVCC	Logic Supply Voltage	Р	
30-32	NC	No connection		
33	GND	Power ground	Р	

I: input, O: output, P: Power

### 3. Operating Specification

#### 3.1 ABSOLUTE MAXIMUM RATINGS

ltem	Symbol	Val	ues	Unit	Remark	
item	Symbol	Min.	Max.	Onic	Remark	
Power Voltage	V <sub>CC</sub>	-0.3	4.8	V		
Operation Temperature	T <sub>OP</sub>	-20	70	°C		
Storage Temperature	T <sub>ST</sub>	-30	80	°C		
LED Reverse Voltage	V <sub>R</sub>	-	1.2	V	Each LED Note 2	
LED Forward Current	١ <sub>F</sub>		25	mA	Each LED	

**Note 1**: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: V<sub>R</sub> Conditions: Zener Diode 20mA

#### 3.1.1 Typical Operation Conditions

ltem	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Unit	Remark
Power Voltage	V <sub>CC</sub>	2.5	2.75	4.8	V	
10	IOVCC	1.65	1.8	3.7		
Input Logic High Voltage	V <sub>IH</sub>	0.7*IOVCC		IOVCC	V	
Input Logic Low Voltage	V <sub>IL</sub>	0		0.3*IOVCC	V	

#### 3.1.2 Backlight driving conditions

ltem	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Unit	Remark
Voltage for LED Backlight	VL		3.2		V	
Current for LED Backlight	ΙL		30		mA	
LED life time		30000			Hr	Note 1

**Note 1**: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I<sub>L</sub> =30mA.

#### **3.3 Timing Characteristics**

#### 3.3.1 Parallel Interface Characteristics: 18, 16, 9 or 8-bit Bus (8080 Series MCU Interface)

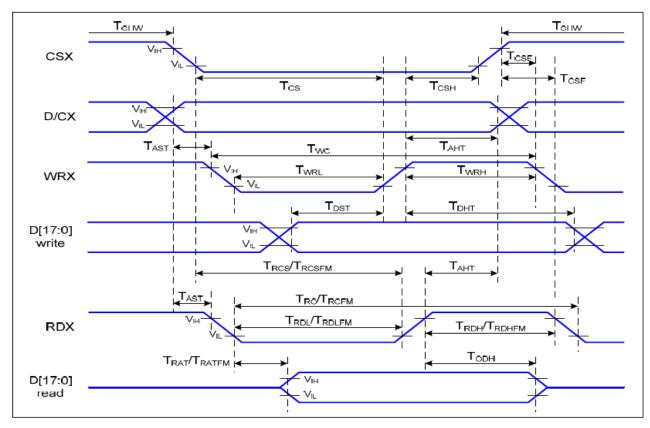


Figure 1 Parallel Interface Timing Characteristics (8080 Ceries MCU Interfa

Signal	Symbol	Parameter	Min	Мах	Unit	Description
D/CX	TAST	Address Setup Ttime	0		ns	
DICX	TAHT	Address Hold Time (Write/Read)	10		ns	-
	TCHW Chip Select "H" Pulse Width		0		ns	
	TCS	CS Chip Select Setup Time (Write)			ns	
csx	TRCS	Chip Select Setup Time (Read ID)	45		ns	
037	TRCSFM	Chip Select Setup time (Read FM)	355		ns	-
TCSF TCSH		Chip Select Wait Time (Write/Read)	10		ns	
		Chip Select Hold Time	10		ns	
	TWC	Write Cycle	66		ns	
WRX	TWRH	Control Pulse "H" Duration	15		ns	
	TWRL	Control Pulse "L" Duration	15		ns	
	TRC	Read Cycle (ID)	160		ns	
RDX (ID)	TRDH	Control Pulse "H" Duration (ID)	90		ns	When Read ID Data
	TRDL	Control Pulse "L" Duration (ID)	45		ns	

Ta=25 ℃, VDDI=1	1 65~3 7V VD	D=25~48V

# ATM0177B5(ZETTLER DISPLAYS)TFT MODULE VER3.2

RDX	TRCFM	Read Cycle (FM)	450		ns	When Read from
(FM)	TRDHFM	Control Pulse "H" Duration (FM)	90		ns	Frame Memory
( ( 1 1 1 1 )	TRDLFM	Control Pulse "L" Duration (FM)	355		ns	Frame Memory
	TDST	Data Setup Time	10		ns	
	TDHT	Data Hold Time	10		ns	
D[17:0]	TRAT	Read Access Time (ID)		40	ns	For CL=30pF
	TRATFM	Read Access Time (FM)		340	ns	
	TODH	Output Disable Time	20	80	ns	

8080 Parallel Interface Characteristics

# 4.0 OPTICAL SPECIFICATIONS

Item	Symbol	Condition	Values			Unit	Remark
item	Symbol	Condition	Min.	Тур.	Max.	Onit	Nelliark
	θL	Φ=180°(9 O'CLOCK)	35	45			Note 1
Viewing Angle	θ <sub>R</sub>	Φ=0°(3 O'CLOCK)	35	45		dograa	
(CR <b>≥10</b> )	θτ	Φ=90°(12 O'CLOCK)	35	45		degree	
	θΒ	Φ=270°(6 Ο'CLOCK)	10	20			
Response Time	T <sub>ON</sub> +T <sub>OF</sub>			8	16	-msec	Note 3
Response nine	F			0	10	-11560	Note 3
Contrast Ratio	CR		400	500			Note 4
	W <sub>x</sub>	Normal	0.26	0.31	0.36		Note 2
Color Chromaticity	W <sub>Y</sub>	Θ=Φ=0°	0.28	0.33	0.38		Note 5
	۷۷Y		0.20	0.33	0.50		Note 6
Luminance	L		180	240		cd/m <sup>2</sup>	Note 6
Luminance Uniformity	YU		75	80		%	Note 7

**Test Conditions:** 

1. If=30mA, the ambient temperature is 25  $^\circ\!\!{\rm C}.$ 

2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

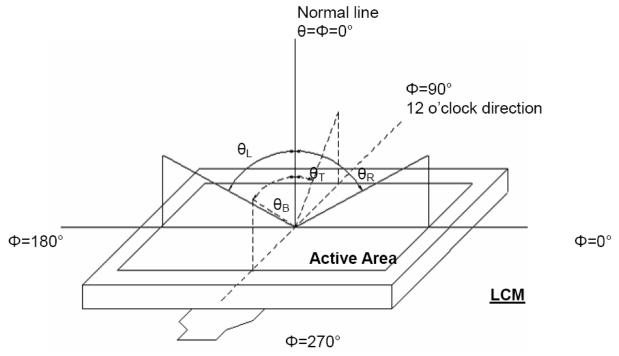


Figure 4.1 Definition of viewing angle.

#### Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON

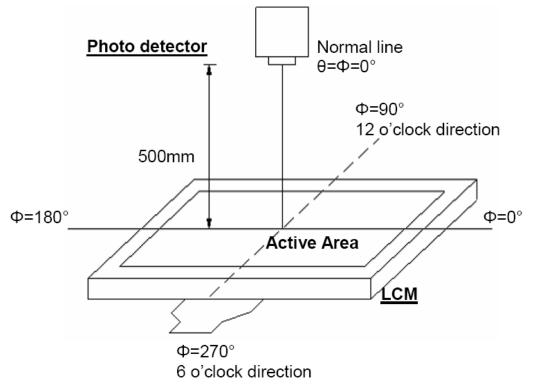


Figure 4.2 Optical measurement system setup

#### Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.

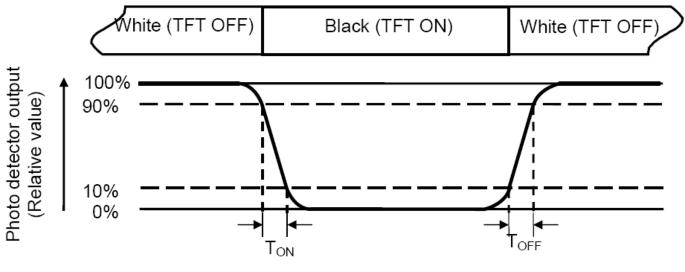


Figure 4.3 Definition of response.

#### ATM0177B5(ZETTLER DISPLAYS)TFT MODULE VER3.2

#### Note 4: Definition of contrast ratio

 $Contrast ratio(CR) = \frac{Luminance measured when LCD on the "white" state}{Luminance measured when LCD on the "black" state}$ 

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4 ). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) = 
$$\frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

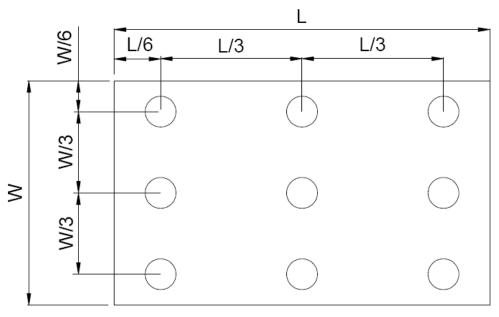


Figure 4.3 Definition of measuring points.

Bmax: The measured maximum luminance of all measurement position. Bmin: The measured minimum luminance of all measurement position.

# **5. RELIABILITY TEST**

ltem	Test Condition Item	Remark
High temperature storage	Ta= 70 °C 240hrs	Note 1 Note 4
Low temperature storage	Ta=-20 °C 240hrs	Note 1 Note 4
High temperature operation	Ts= 60 °C 240hrs	Note 2 Note 4
Low temperature operation	Ts=-10 °C 240hrs	Note 1 Note 4
High temperature/High humidity operation	90% RH 40°C 120hrs	Note 4
Thermal Shock	-1°C/30 min ~ +6°C/30 min for a total 10 cycles, Start with cold temperature and end with high temperature.	Note 4
Package drop test	Height:100 cm 1 corner, 3 edges, 6 surfaces	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the

display function. After the reliability test, the product only guarantees operation,

but don't guarantee all of the cosmetic specification.

**Note 4**: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

#### 6. PRECAUTION FOR USING LCM

- 1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
- 2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
- 5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
- 6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
- 8. Mechanical Considerations
  - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
  - b) Do not tamper in any way with the tabs on the metal frame.
  - c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
- 9. Static Electricity
  - a) Operator

Wear the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth:  $1x10^8$  ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth:  $1x10^8$  ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH. e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Soldering

Soldering anything to this TFT display would void the warranty.

g) Others

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge.

- 10. Operation
  - a) Driving voltage should be kept within specified range; excess voltage shortens display life.
  - b) Response time increases with decrease in temperature.
  - c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
  - d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.

# 7. MECHANICAL DRAWING

