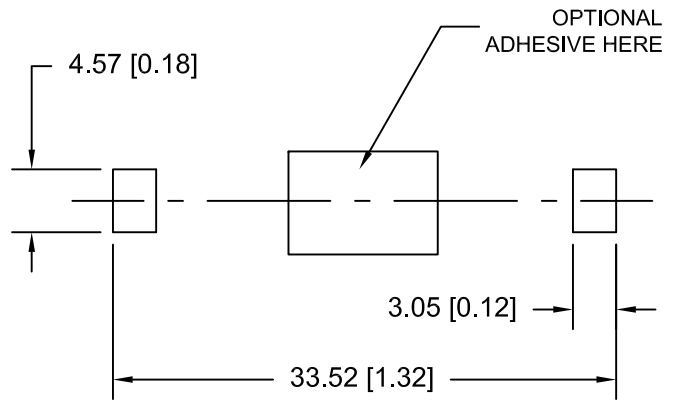
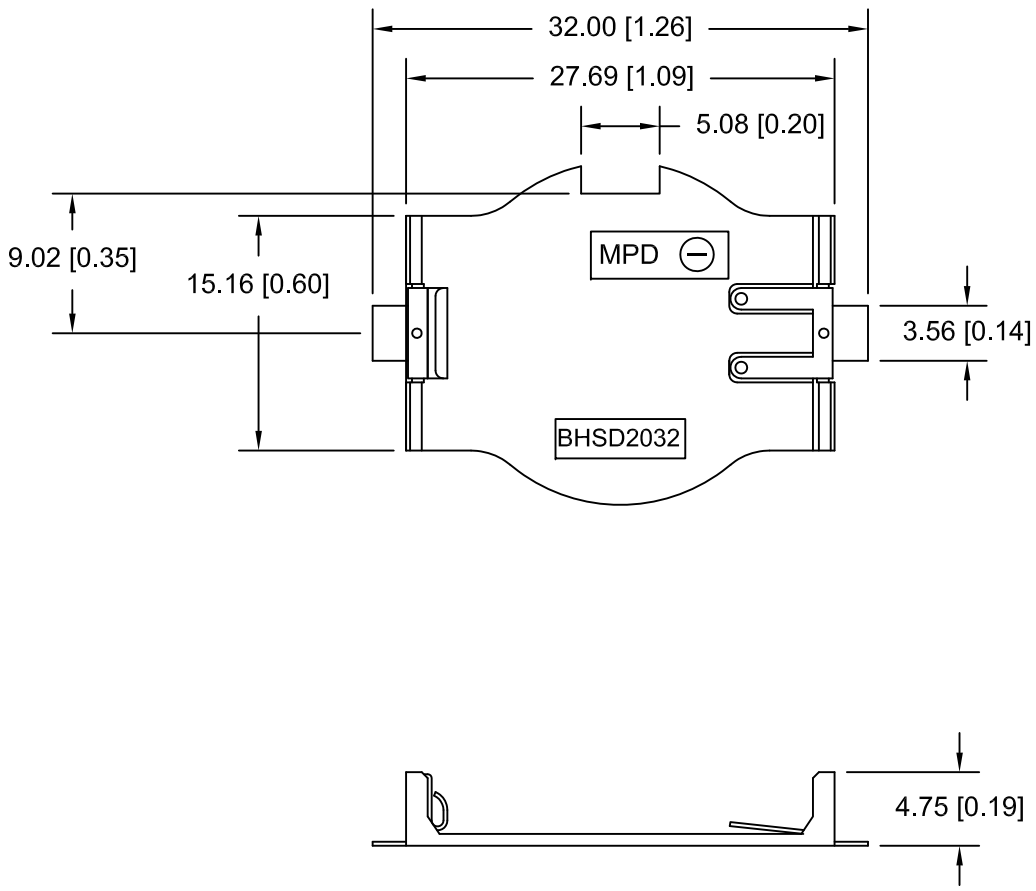


Date	SYM	REVISION RECORD	AUTH	DR.	CK.
09/04/15	G	DIM. 3.05 WAS 2.79 DIM. 4.57 WAS 4.06 ADDED DIM. 33.52 AND REMOVED DIM. 29.30 ADDED NOTCH	T.B.	B.S.	
12/11/15	H	Added Patent no.	T.B.	B.S.	
01/27/17	J	Updated UL File number	T.B.	B.S.	




PCB LAYOUT

SPECIFICATIONS:

- * Holder material- LCP Zenite, Color Off white, UL File No. E344082
- * Contacts : Phosphor Bronze C5191, Gold Flash, 1u - 3u thickness
- * Temperature range: -40°C to +280°C
- * Vibration Test: 10-50-10 Hz swept in 60 seconds
X,Y,Z direction: 1 hour each direction
- * Result: No dislodgement of the cell or damage
No discontinuity over 10 micro-seconds
- * Shock Test: Half sine wave acceleration 150G. 6 m/s
3 shock pulses each direction per axis, total of 18 pulses
- * Result: No evidence of physical damage
- * Drop Test: 1meter - No dislodgement of cell
- * ROHS 2 Compliant

PAT: 9,192,059

Tolerance (Inches)	 Memory Protection Devices, Inc 200 Broad Hollow Road, Farmingdale, New York 11735		
Decimal ±.5 (.020)	Scale	Drawn By B.S.	
Fractional ±.8 (.030)	Title BASE, CR2032 SURFACE MOUNT BATTERY		
Angular ± 3°	Date 01/27/17	Drawing Number BHSD-2032-SM	
		Approved By T.B.	