



October 2011

2N3904 / MMBT3904 / PZT3904 NPN General Purpose Amplifier

Features

- This device is designed as a general purpose amplifier and switch.
- The useful dynamic range extends to 100 mA as a switch and to 100 MHz as an amplifier.



Absolute Maximum Ratings* T_a = 25°C unless otherwise noted

Symbol	Parameter	meter Value	
V _{CEO}	Collector-Emitter Voltage	40	V
V _{CBO}	Collector-Base Voltage	60	V
V_{EBO}	Emitter-Base Voltage	6.0	V
I _C	Collector Current - Continuous	200	mA
T _{J,} T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

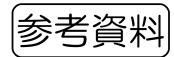
- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics T_a = 25°C unless otherwise noted

Symbol	Parameter	Max.			Units
		2N3904	*MMBT3904	**PZT3904	Uiillo
P _D	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	1,000 8.0	mW mW/°C
$R_{ heta JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

^{*} Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06".

^{**} Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm².



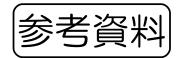
Electrical Characteristics $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
OFF CHARAC	CTERISTICS			II.	l.
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	$I_C = 1.0 \text{mA}, I_B = 0$	40		V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_C = 10\mu A, I_E = 0$	60		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_E = 10\mu A, I_C = 0$	6.0		V
I _{BL}	Base Cutoff Current	$V_{CE} = 30V, V_{EB} = 3V$		50	nA
I _{CEX}	Collector Cutoff Current	$V_{CE} = 30V, V_{EB} = 3V$		50	nA
ON CHARAC	TERISTICS*				•
h _{FE}	DC Current Gain	$ \begin{aligned} & I_{C} = 0.1 \text{mA}, V_{CE} = 1.0 \text{V} \\ & I_{C} = 1.0 \text{mA}, V_{CE} = 1.0 \text{V} \\ & I_{C} = 10 \text{mA}, V_{CE} = 1.0 \text{V} \\ & I_{C} = 50 \text{mA}, V_{CE} = 1.0 \text{V} \\ & I_{C} = 100 \text{mA}, V_{CE} = 1.0 \text{V} \end{aligned} $	40 70 100 60 30	300	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 10$ mA, $I_B = 1.0$ mA $I_C = 50$ mA, $I_B = 5.0$ mA		0.2 0.3	V V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 10$ mA, $I_B = 1.0$ mA $I_C = 50$ mA, $I_B = 5.0$ mA	0.65	0.85 0.95	V V
SMALL SIGN	AL CHARACTERISTICS				
f⊤	Current Gain - Bandwidth Product	$I_C = 10$ mA, $V_{CE} = 20$ V, $f = 100$ MHz	300		MHz
C _{obo}	Output Capacitance	$V_{CB} = 5.0V, I_{E} = 0,$ f = 1.0MHz		4.0	pF
C _{ibo}	Input Capacitance	$V_{EB} = 0.5V, I_{C} = 0,$ f = 1.0MHz		8.0	pF
NF	Noise Figure	$I_{C} = 100 \mu A, V_{CE} = 5.0 V,$ $R_{S} = 1.0 k \Omega,$ f = 10 Hz to 15.7 kHz		5.0	dB
SWITCHING (CHARACTERISTICS			•	•
t _d	Delay Time	$V_{CC} = 3.0V, V_{BE} = 0.5V$		35	ns
t _r	Rise Time	$I_C = 10 \text{mA}, I_{B1} = 1.0 \text{mA}$		35	ns
t _s	Storage Time	$V_{CC} = 3.0V, I_{C} = 10mA,$		200	ns
t _f	Fall Time	$I_{B1} = I_{B2} = 1.0 \text{mA}$		50	ns

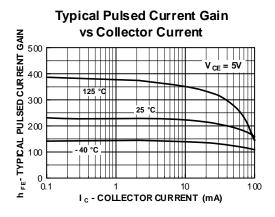
^{*} Pulse Test: Pulse Width $\leq 300 \mu s, \ Duty \ Cycle \leq 2.0\%$

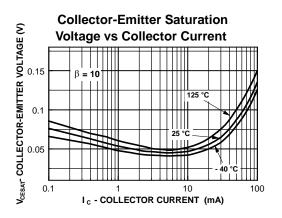
Ordering Information

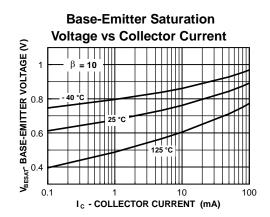
Part Number	Marking	Package	Packing Method	Pack Qty
2N3904BU	2N3904	TO-92	BULK	10000
2N3904TA	2N3904	TO-92	AMMO	2000
2N3904TAR	2N3904	TO-92	AMMO	2000
2N3904TF	2N3904	TO-92	TAPE REEL	2000
2N3904TFR	2N3904	TO-92	TAPE REEL	2000
MMBT3904	1A	SOT-23	TAPE REEL	3000
MMBT3904_D87Z	1A	SOT-23	TAPE REEL	10000
PZT3904	3904	SOT-223	TAPE REEL	2500

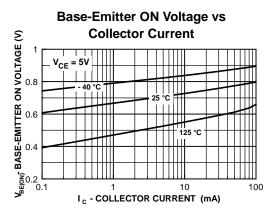


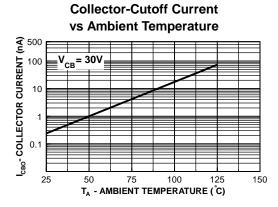
Typical Performance Characteristics

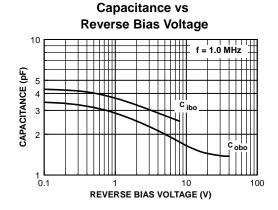


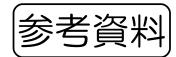




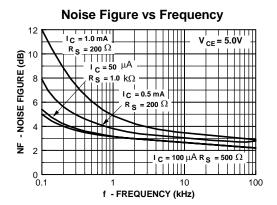


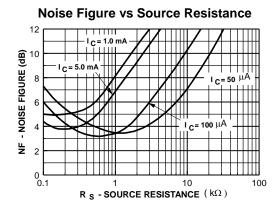


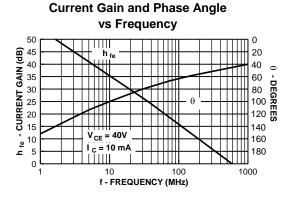


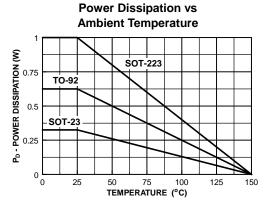


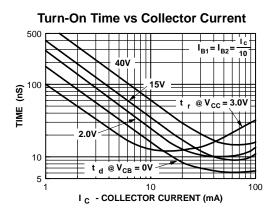
Typical Performance Characteristics (continued)

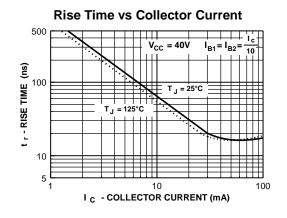






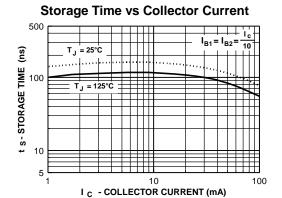


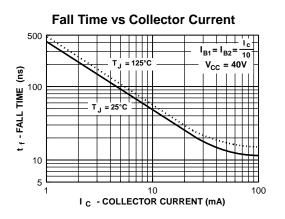


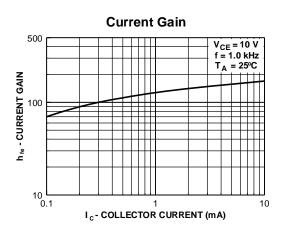


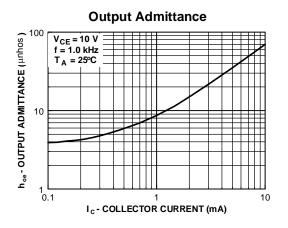


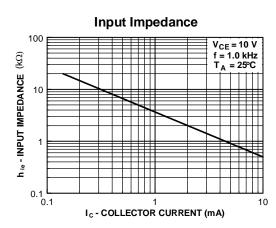
Typical Performance Characteristics (continued)

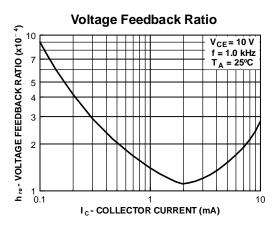


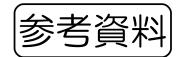












Test Circuits

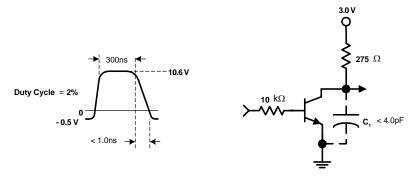


FIGURE 1: Delay and Rise Time Equivalent Test Circuit

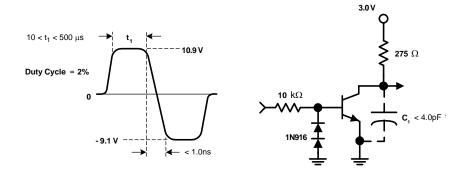


FIGURE 2: Storage and Fall Time Equivalent Test Circuit





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Definition of Terms				
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