NGX parallel port JTAG

How to Install H-JTAG:

User can download the latest version (the document is in reference with the H-JTAF V0.9.2) installation file from www.hjtag.com. Normally, the downloaded file is zipped and need to be unzipped first. After unzipped, user can get the installation exe file h-jtag.exe. Correct setting is done by referring the User manual that is given in installation path (C:\Program Files\H-JTAG\DOC). Select lpc2148 as target for blue board.

How to install KEIL:

User can download the latest version (the document is in reference with the mdk380a) installation file from http://www.keil.com and install it.

Configuration of H-JTAG in KEIL:

This section introduces how to configure the keil to work with the H-JTAG. The H-JTAG V0.9.2 uses the AGDI interface under keil.

First, run TOOLCONF.EXE located under the installation direction of H-JTAG or the icon which you can see on the desktop as in figure 1.

Figure 1

The purpose is to update the configuration file (TOOS.INI) of KEIL. In TOOLCONF.EXE, use the browse button to locate the configuration file TOOLS.INI, which can be found under the installation direction of KEIL, as shown in below. Then click the config button to perform the update as in figure 2. After the update is completed successfully, click Exit to exit TOOLCONF.EXE.

Figure 2

Next start KEIL and open a project. Then click Project → options for target as shown in figure 3

Figure 3

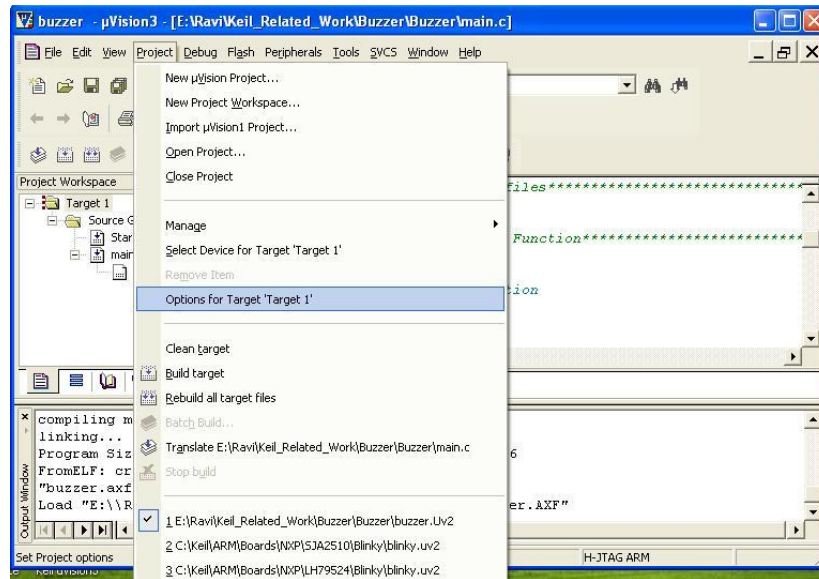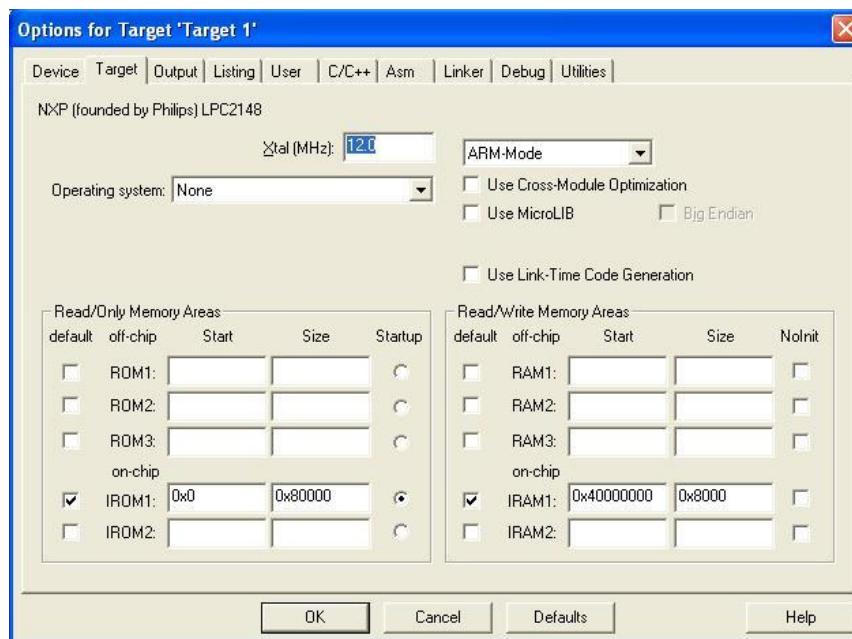Then a dialog of options will appear as in figure 4.

Figure 4.

Active the device page in the option dialog. Select the target processor which you are using (here in this case select LPC2148) as in the figure 5.
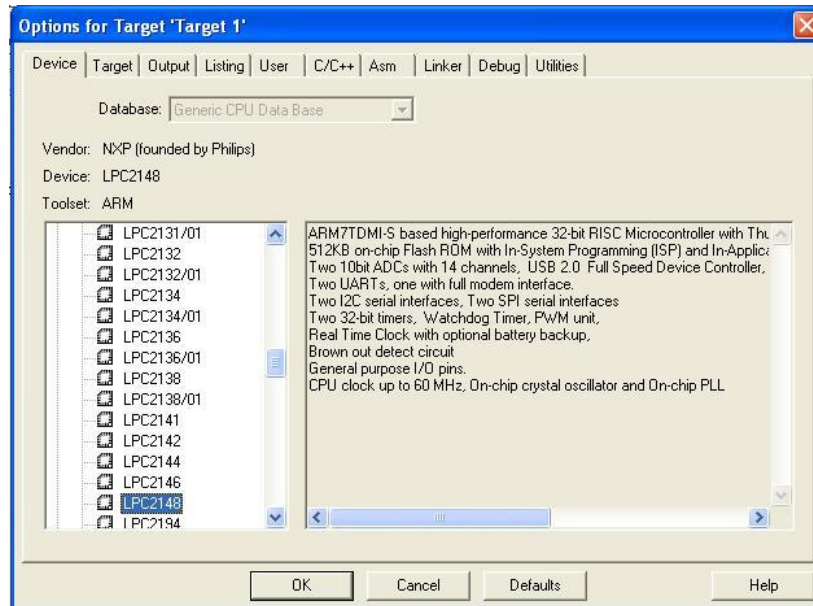

Figure 5.

Active the debug page in option dialog. In the Debug page, click the drop-down button, two debug drivers, H-JTAG ARM and H-JTAG CORTEX-M3, can be found on the list, as in figure 6. User should select one of the drivers' accordingly. Here user should select H-JTAG ARM.
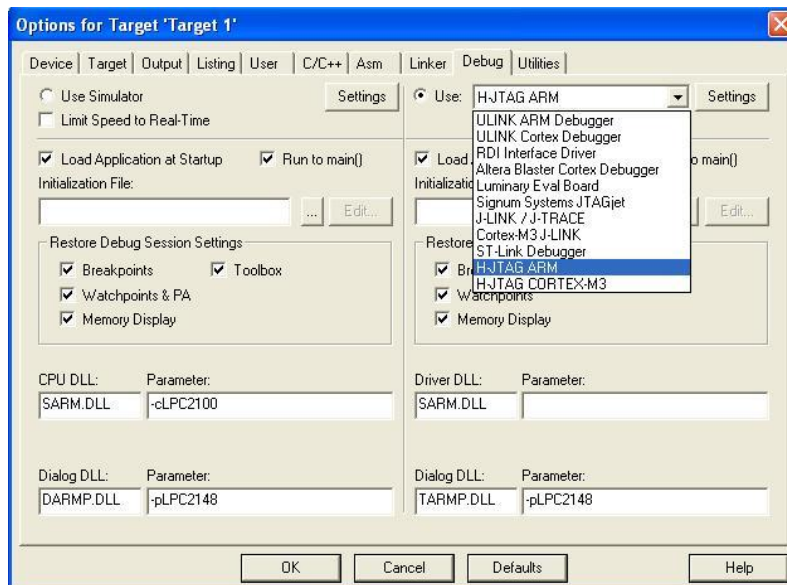

Figure 6.
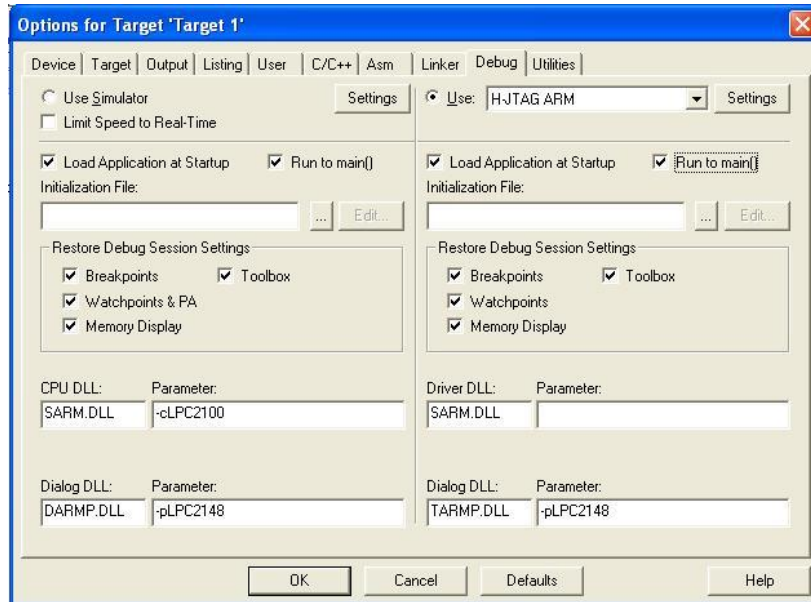
This should look as in the figure 7.

Figure 7.

Active the utilities in option dialog. In the utilities page click the drop down button, two debug drivers, H-JTAG ARM and H-JTAG CORTEX-M3, can be found on the list, as in the figure 8. User should select one of the drivers' accordingly. Here user should select H-JTAG ARM.
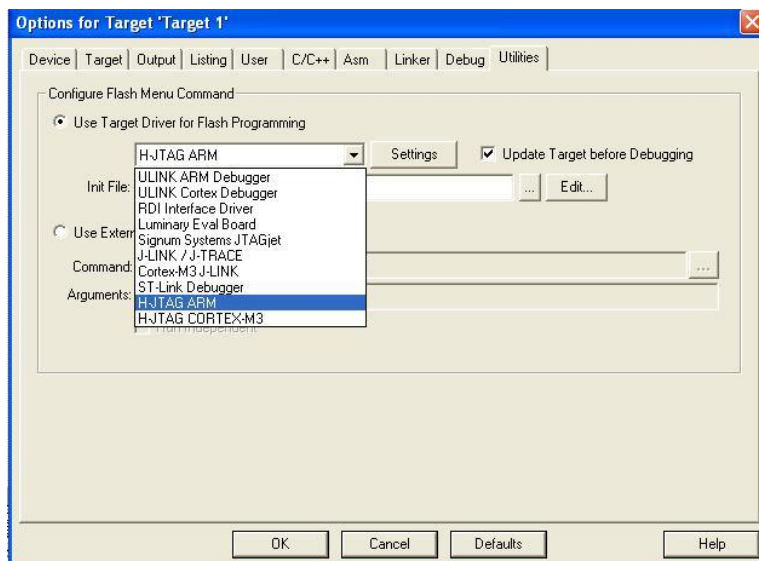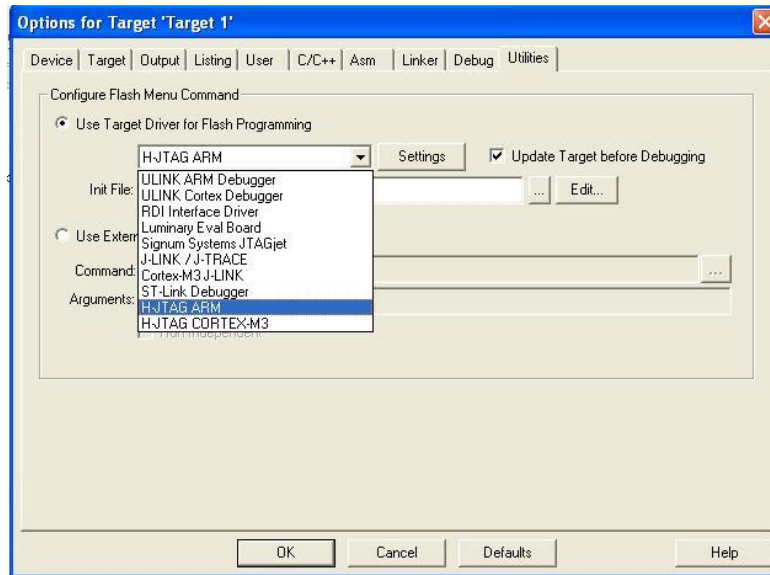

Figure 8

This should look as in figure 9.

Figure 9.

Then, click OK to complete the configuration.

Then click on the debug → start/stop debug session as shown in figure 10.
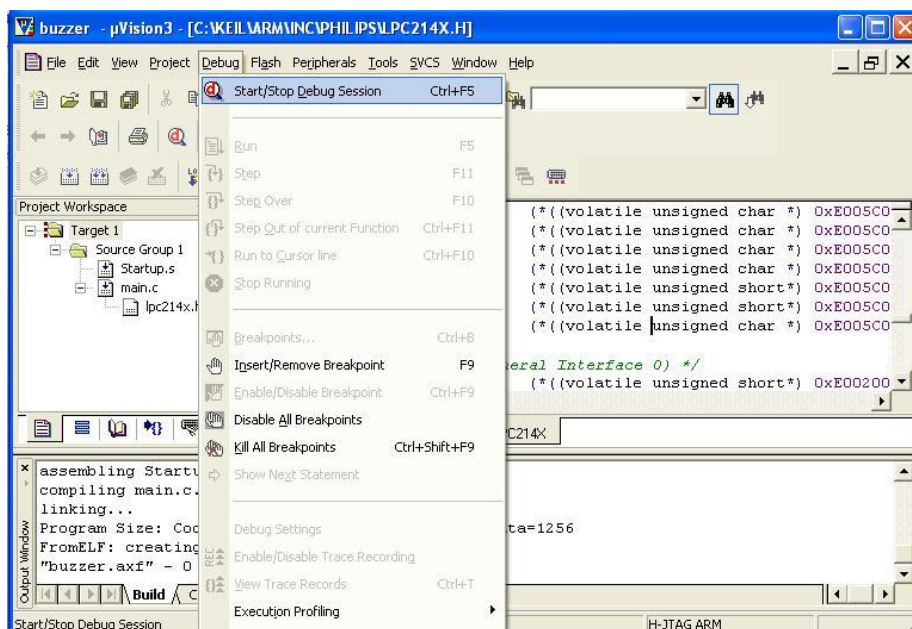

Figure 10.

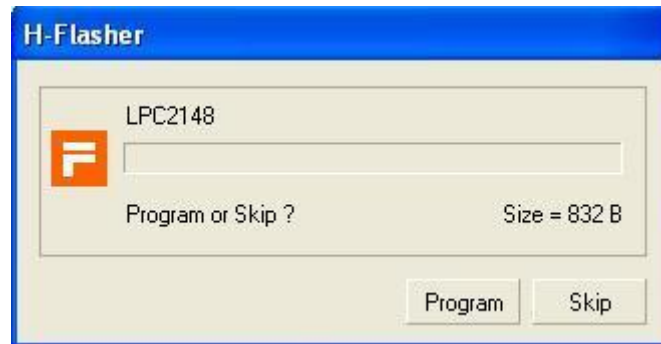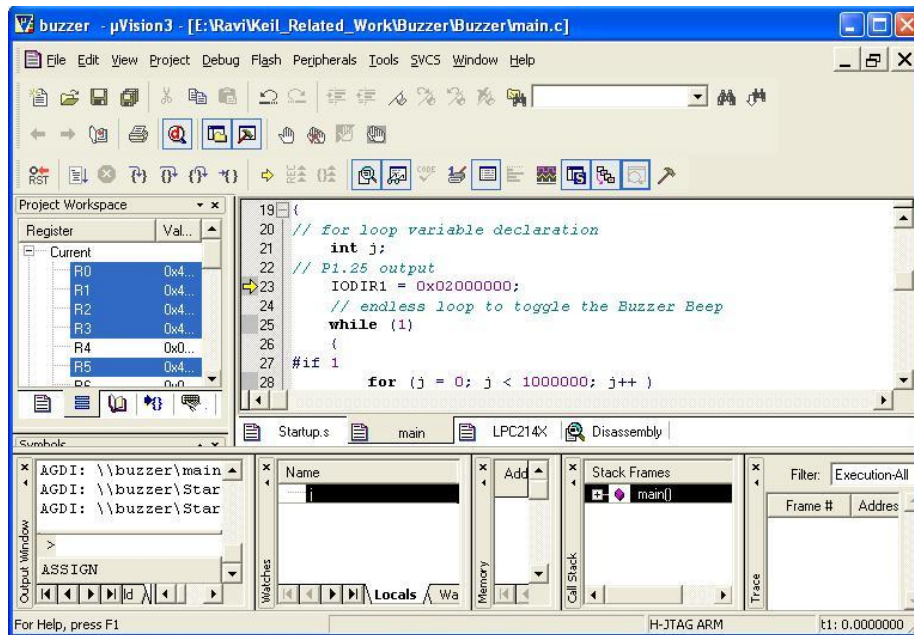Then a dialog of H-Flasher will appear as shown in figure 11

Figure 11.

Click on the program tab to program the target board. The cursor will point to the main as in figure 12.

We are now all set to debug the program with NGX parallel port JTAG module.